

LCM Specification

Preliminary specification

Final Specification

Project No. 项目编号	H055A1HDIIL3N30		
Customer 客户名称			
Module No. 客户型号			
Product type 产品内容	TFT LCD Module 720 x 3RGB x 1280 Dots 5.5" TFT LCD		
Signature by customer: 客户确认签章:			
<input type="checkbox"/> Trial production		<input type="checkbox"/> Mass production	
编 制	电子审核	结构审核	批 准
Y. L			

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Shenzhen Hot Display Technology Co., Ltd

1 Document revision history :

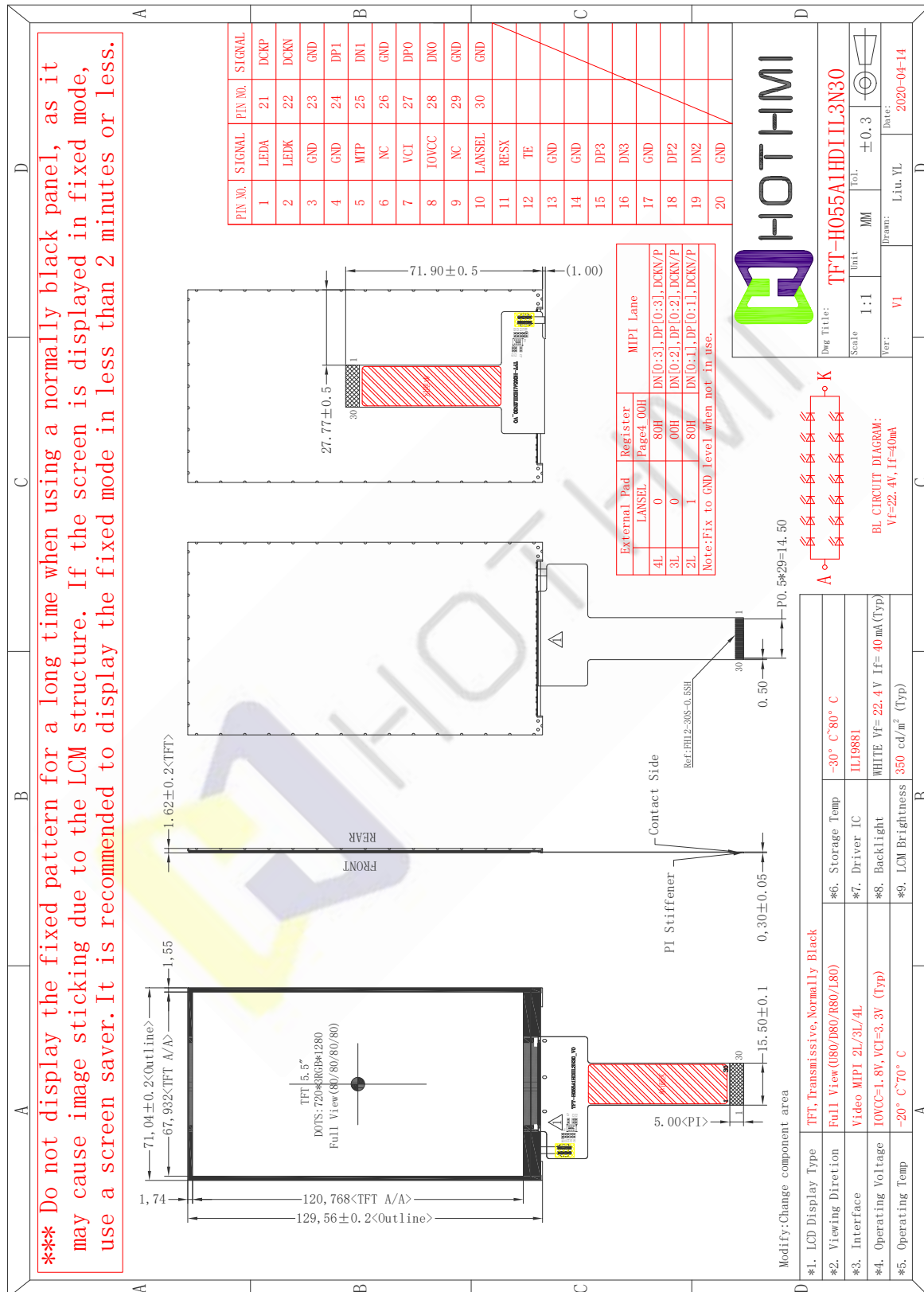
DOCUMENT REVISION	DATE	DESCRIPTION	PREPARED BY	APPROVED BY
0	2020-8-24	First Release.	Y.L	



1. General Feature:

Item	Standard Value	Unit
Display Size	5.5"	--
Number of Pixels	720(H)x3(RGB)*1280(V)	--
Active Area	67.932(H) *120.768(V)	mm
Outline Dimension	71.04(H) ×129.56(V)× 1.62(D)	mm
Viewing Direction	FULL O'clock	-
Interface	MIPI Video Mode - 2/3/4L	-
LCM Driver IC	ILI9881D	-
LCM Driver Condition	IOVCC=1.8V,VCI=2.8V (Typ)	V
Backlight	White LED	-
Touch Panel	Without Touch Panel	-
CTP Driver IC	---	
CTP Driver Condition	---	
Operation Temperature	-20~70	°C
Storage Temperature	-30~80	°C
Pixels Per Inch	267	-

2. Outline Dimensions

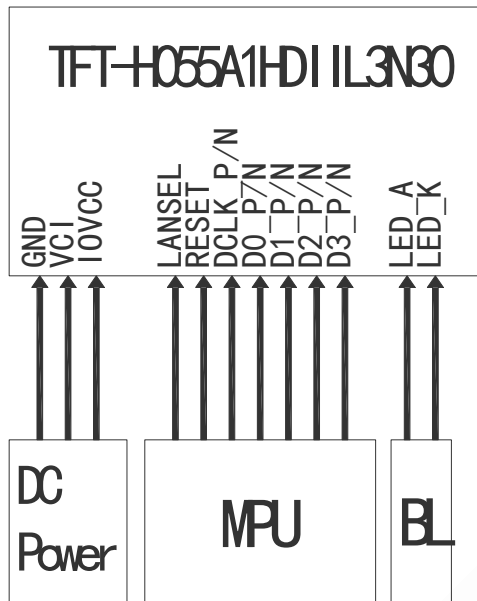


3. Pin Description

3.1 Pin Description

Pin NO.	Symbol	Description
1	LED_A	LED Anode
2	LED_K	LED Cathode
3	GND	Ground
4	GND	Ground
5	MTP	No Connect
6	NC	No Connect
7	VCI	Analog Power
8	IOVCC	Power supply for I/O system.
9	NC	No Connect
10	LANSSEL	MIPI DSI Lane number selection pin.
11	RESX	Reset Pin
12	TE	No Connect
13	GND	Ground
14	GND	Ground
15	D3P	Positive polarity of low voltage differential data signal.
16	D3N	Negative polarity of low voltage differential data signal.
17	GND	Ground
18	D2P	Positive polarity of low voltage differential data signal.
19	D2N	Negative polarity of low voltage differential data signal.
20	GND	Ground
21	DCLKP	Positive polarity of low voltage differential clock signal.
22	DCLKN	Negative polarity of low voltage differential clock signal.
23	GND	Ground
24	D1P	Positive polarity of low voltage differential data signal.
25	D1N	Negative polarity of low voltage differential data signal.
26	GND	Ground
27	D0P	Positive polarity of low voltage differential data signal.
28	D0N	Negative polarity of low voltage differential data signal.
29	GND	Ground
30	GND	Ground
---END---		

3.2 Wiring Diagram



External Pad	Register	MIPI Lane	
	LANSSEL	Page4_OCH	
4L	0	80H	DN[0:3], DP[0:3], DCKVP
3L	0	00H	DN[0:2], DP[0:2], DCKVP
2L	1	80H	DN[0:1], DP[0:1], DCKVP

Note: Fix to GND level when not in use.

4. Electrical Characteristics

4-1 TFT LCD Module Operating Conditions

Item	Symbol	Condition	Min	Type	Max	Unit
Interface logic circuits	IOVCC	-	1.75	1.8	3.3	V
Analog Power supply	VCI	-	2.65	2.8	3.3	V
TFT Gate on voltage	VGH	-	10.0	-	15.0	V
TFT Gate off voltage	VGL	-	-10.0	-	-13.0	V

4-2 LED back light specification (per chip)

Item	Symbol	Condition	Min	Type	Max	Unit
Forward voltage	Vt	If=20mA	21.0	22.4	23.8	V
Forward current	Ipn	/1-chip	-	40	-	mA
Luminance(With LCD)	Lv	If=40mA	-	350	-	cd/m ²
Luminous color	White					

5. OPTICAL SPECIFICATION

5.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance 1lux and temperature = 25 ± 2°C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. The center of the measuring spot on the Display surface shall stay fixed. The backlight should be operating for 30 minutes prior to measurement.

5.2 Optical Specifications

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing Angle Range	Horizontal	Θ L	CR>10	-	80	-	Deg.	Note 1
		Θ R		-	80	-	Deg.	
	Vertical	Θ U		-	80	-	Deg.	
		Θ D		-	80	-	Deg.	
Contrast ratio		CR	$\Theta = 0^\circ$	1000	1200	-		Note2
Color Gamut		CG		-	-	-	%	
White Chromaticity		Wx		-	(0.300)	-		
		Wy		-	(0.331)	-		
Reproduction of color	Red	Rx	$\Theta = 0^\circ$	-	(0.655)	-		Note4 (Based on C Light)
		Ry		-	(0.320)	-		
	Green	Gx		-	(0.268)	-		
		Gy		-	(0.584)	-		
	Blue	Bx		-	(0.137)	-		
		By		-	(0.095)	-		
Response Time (Rising + Falling)		Tr+Tf	$\Theta = 0^\circ$ Ta= 25°C	-	25	-	ms	Note5
Transmittance(with Polarizer)		Tr		-	(4.6)	-	%	Note3

Note:

1.Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o' clock direction and the vertical or 6, 12 o' clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).

2.Contrast measurements shall be made at viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black)

state . (see FIGUR 1) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. Transmittance is the Value without APF and without CG.

4. The color chromaticity coordinates specified in the above table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.

5. The electro-optical response time measurements shall be made as FIGURE 2 by switching the “data” input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is T_r , and 90% to 10% is T_f .

Figure1 Measurement Set Up

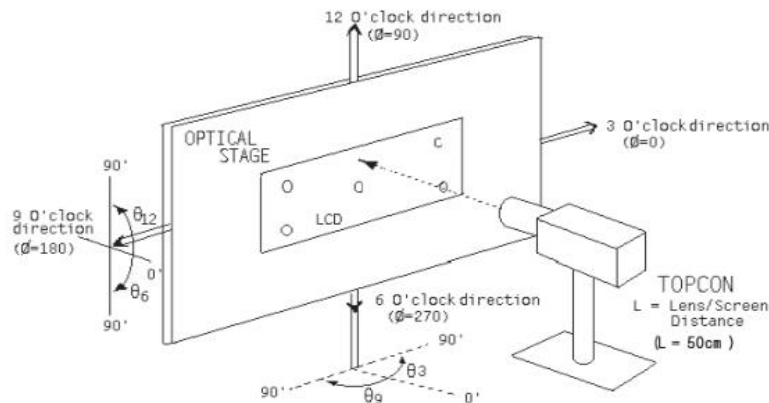
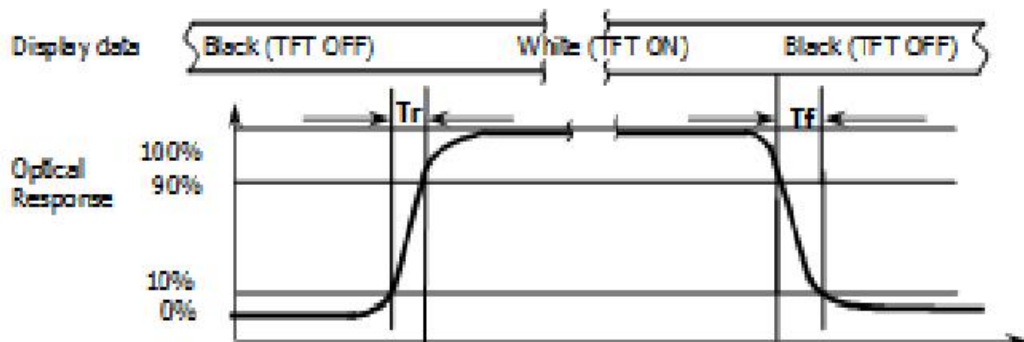
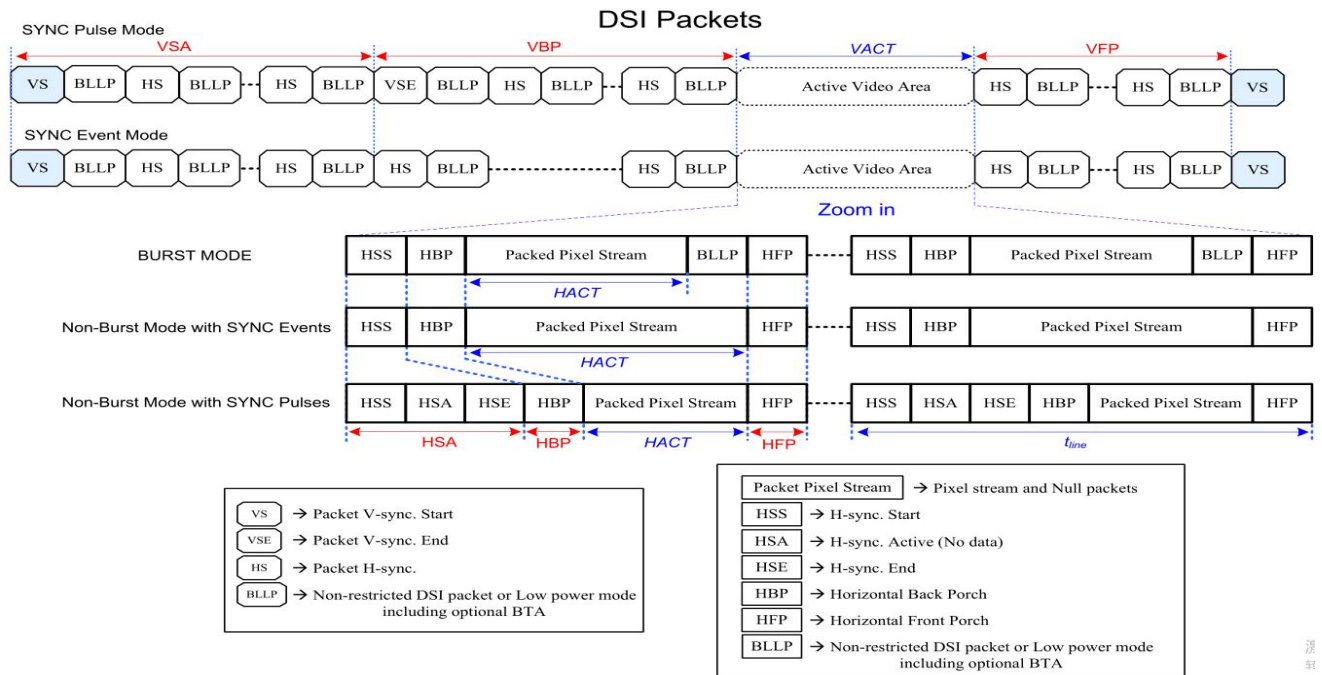


Figure2 Response Time Testing



6.MIPI Interface Characteristics

6-1 Timing For DSI Video Mode



Parameters	Symbols	Min.	Typ.	Max.	Units
Vertical sync. active	VSA	2 (Note 6)	-	-	Line
Vertical Back Porch	VBP	14 (Note 6)	-	-	Line
Vertical Front Porch	VFP	8 (Note 6)	-	-	Line
Active lines per frame	VACT	-	1280	-	Line
Horizontal sync. active	HSA	2	-	-	Pixel
Horizontal Porch period	HSA + HBP + HFP	1.6	-	-	us
Active pixels per line	HACT	-	720	-	Pixel
Bit rate	BR _{bps}	385		Note 5	Mbps/lane

1 UI=1/Bit rate

HAS(pixel)= (tHSA*lane number) / (UI* pixel format)

HBP(pixel)= (tHBP*lane number) / (UI* pixel format)

HFP(pixel)= (tHFP*lane number) / (UI* pixel format)

$$\text{Frame Rate} = \frac{\text{BR}_{\text{bps}} \times \text{Lane}_{\text{num}}}{(\text{VACT} + \text{VSA} + \text{VBP} + \text{VFP}) \times (\text{HACT} + \text{HSA} + \text{HBP} + \text{HFP}) \times \text{Pixel Format}}$$

Example : BR_{bps} = 457Mbps/lane, 1UI=2.1883ns, Frame rate=60Hz, VACT=1280, VSA=2, VBP=30, VFP=20, HACT=720, HSA=33, HBP=100, HFP=100, Lane_{num}=4(lane), Pixel Format=24(bit).

Note:

1. Lane_{num}: Data lane of MIPI-DSI.
2. Pixel Format: Please reference to "4.1DSI System Interface".
3. The formula exists slightly error because of the host-transmission way.
4. The best frame rate setting : 2 data lanes : 50~60 Hz / 3 data lanes : 50~70 Hz / 4 data lanes : 50~70 Hz.
5. Please reference to "Table 39: Limited Clock Channel Speed".
6. The minimum values of this table mean the limitation of IC without considering the panel GIP. The actual values of VSA, VBP and VFP will be changed by different panel GIP setting.

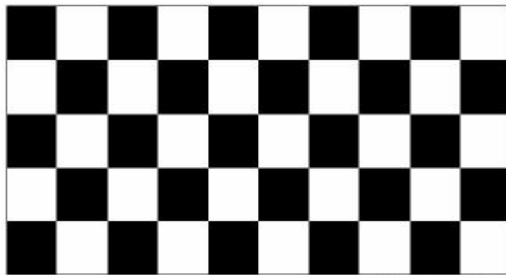
7. RELIABILITY TEST

7-1 Temperature and Humidity

TEST ITEMS	CONDITIONS	NOTE
High Temperature Storage	Ta=+80 o C, 120hrs	
Low Temperature Storage	Ta=-30 o C, 120hrs	
High Temperature Operation	Ta=+70 o C, 120hrs	
Low Temperature Operation	Ta=-20 o C, 120hrs	
High Temperature and High Humidity (Operating)	Ta=+60 o C, 90%RH, 120hrs	

Note: (1) All tests above are practiced at module type.

(2) There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.



(a) Test Pattern (chess board Pattern)



(b) Gray Pattern

7-2 Shock and Vibration

ITEMS	CONDITIONS
Packing Shock (Non-Operation)	<ul style="list-style-type: none"> ● Shock level:980m/s² ● Waveform:1/2 Sine wave,6msec ● ±X, ±Y ±Z,each axis 1 times
Packing Vibration (Non-Operation)	<ul style="list-style-type: none"> ● Frequency range:8-33.3HZ ● Stoke:1.0mm ● Sweep: 10Hz-50Hz ● x,y,z 2 hours for each direction

7-3 Electrostatic Discharge

TEST ITEM	CONDITIONS
ESD (Non-operation)	150pF,330 Ω , Contact±4KV,Air :±8KV.Note 1
	200pF,0 Ω , ±200V Contact test.Note 2

Note:Measure Point:

- 1.LCD glass and metal bezel
- 2.IF connector pins

8.HANDDLING & CAUTIONS

8-1 Caution For Operation

◆Since the LCM is made of glass, do not apply strong mechanical impact or static load onto it. Handling with care since shock, vibration, and careless handling may seriously affect the product. If it falls from a high place or receives a strong shock, the glass maybe broken.

◆It is indispensable to drive the LCM within the specified voltage limit since the higher voltage than the limit causes LCM's life shorter. An electro-chemical reaction due to DC causes undesirable deterioration of the LCM so that the use of DC drive should avoid.

◆Do not connect or disconnect the LCM to or from the system when power is on.

◆Never use the LCM under abnormal conditions of high temperature and high humidity.

◆When expose to drastic fluctuation of temperature(hot to cold or cold to hot), the LCM may be affected; specifically, drastic temperature fluctuation from cold to hot, produces dew on the LCM's surface which may affect the operation of the polarizer on the LCM.

◆Response time will be extremely delay at lower temperature than the operating temperature range and on the other hand LCM may turn black at temperature above its operational range. However those phenomenon do not mean malfunction or out of order with the LCM. The LCM will revert to normal operation once the temperature returns to the recommended temperature range for normal operation.

◆ Do not display the fixed pattern for a long time when using a normally black panel, as it may cause image sticking due to the LCM structure. If the screen is displayed in fixed mode, use a screen saver. It is recommended to display the fixed mode in less than 2 minutes or less.

◆Do not disassemble and/or re-assemble LCM module

7-2 Caution Against Static Charge

◆The LCM use C-MOS LSI drivers, so customers are recommended that any unused input terminal would be connected to Vdd or Vss, do not input any signals before power is turn on, and ground you body, work/assembly area, assembly equipments to protect against static electricity.

◆Remove the protective film slowly, keeping the removing direction approximate 30-degree not vertical from panel surface, if possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.

◆Avoid the use work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.

◆In handling the LCM, wear non-charged material gloves. And the conducting wrist to the earth and the conducting shoes to the earth are necessary

9.LCD display initialization code

```
Void Panel_Initial_Code(void)
{
Generic_Long_Write_3P(0xFF,0x98,0x81,0x04);
//GIP_1

Generic_Short_Write_1P(0x01,0x00);
Generic_Short_Write_1P(0x02,0x00);
Generic_Short_Write_1P(0x03,0x73);
Generic_Short_Write_1P(0x04,0x00);
Generic_Short_Write_1P(0x05,0x00);
Generic_Short_Write_1P(0x06,0x0E);
Generic_Short_Write_1P(0x07,0x00);
Generic_Short_Write_1P(0x08,0x00);
Generic_Short_Write_1P(0x09,0x01);
Generic_Short_Write_1P(0x0a,0x01);
Generic_Short_Write_1P(0x0b,0x01);
Generic_Short_Write_1P(0x0c,0x01);
Generic_Short_Write_1P(0x0d,0x01);
Generic_Short_Write_1P(0x0e,0x01);
Generic_Short_Write_1P(0x0f,0x00);
Generic_Short_Write_1P(0x10,0x00 );
Generic_Short_Write_1P(0x11,0x00);
Generic_Short_Write_1P(0x12,0x00);
Generic_Short_Write_1P(0x13,0x00);
Generic_Short_Write_1P(0x14,0x00);
Generic_Short_Write_1P(0x15,0x00);
Generic_Short_Write_1P(0x16,0x00 );
Generic_Short_Write_1P(0x17,0x00 );
Generic_Short_Write_1P(0x18,0x00);
Generic_Short_Write_1P(0x19,0x00);
Generic_Short_Write_1P(0x1a,0x00);
Generic_Short_Write_1P(0x1b,0x00);
Generic_Short_Write_1P(0x1c,0x00);
Generic_Short_Write_1P(0x1d,0x00);
Generic_Short_Write_1P(0x1e,0x40);
Generic_Short_Write_1P(0x1f,0xC0);
Generic_Short_Write_1P(0x20,0x0A);
Generic_Short_Write_1P(0x21,0x05);
```

Generic_Short_Write_1P(0x22,0x00);
Generic_Short_Write_1P(0x23,0x00);
Generic_Short_Write_1P(0x24,0x00);
Generic_Short_Write_1P(0x25,0x00);
Generic_Short_Write_1P(0x26,0x00);
Generic_Short_Write_1P(0x27,0x00);
Generic_Short_Write_1P(0x28,0x33);
Generic_Short_Write_1P(0x29,0x03);
Generic_Short_Write_1P(0x2a,0x00);
Generic_Short_Write_1P(0x2b,0x00);
Generic_Short_Write_1P(0x2c,0x00);
Generic_Short_Write_1P(0x2d,0x00);
Generic_Short_Write_1P(0x2e,0x00);
Generic_Short_Write_1P(0x2f,0x00);
Generic_Short_Write_1P(0x30,0x00);
Generic_Short_Write_1P(0x31,0x00);
Generic_Short_Write_1P(0x32,0x00);
Generic_Short_Write_1P(0x33,0x00);
Generic_Short_Write_1P(0x34,0x00);
Generic_Short_Write_1P(0x35,0x00);
Generic_Short_Write_1P(0x36,0x00);
Generic_Short_Write_1P(0x37,0x00);
Generic_Short_Write_1P(0x38,0x00);
Generic_Short_Write_1P(0x39,0x35);
Generic_Short_Write_1P(0x3A,0x01);
Generic_Short_Write_1P(0x3B,0x40);
Generic_Short_Write_1P(0x3C,0x00);
Generic_Short_Write_1P(0x3D,0x01);
Generic_Short_Write_1P(0x3E,0x00);
Generic_Short_Write_1P(0x3F,0x00);
Generic_Short_Write_1P(0x40,0x35);
Generic_Short_Write_1P(0x41,0x88);
Generic_Short_Write_1P(0x42,0x00);
Generic_Short_Write_1P(0x43,0x40);
Generic_Short_Write_1P(0x44,0x3F);
Generic_Short_Write_1P(0x45,0x20);
Generic_Short_Write_1P(0x46,0x00);

//1F TO 3F_ RESET KEEP LOW ALL GATE ON
//LVD 卑祇 ALL GATE ON :VGH

//GIP_2

Generic_Short_Write_1P(0x50,0x01);
Generic_Short_Write_1P(0x51,0x23);
Generic_Short_Write_1P(0x52,0x45);
Generic_Short_Write_1P(0x53,0x67);
Generic_Short_Write_1P(0x54,0x89);
Generic_Short_Write_1P(0x55,0xAB);
Generic_Short_Write_1P(0x56,0x01);
Generic_Short_Write_1P(0x57,0x23);
Generic_Short_Write_1P(0x58,0x45);
Generic_Short_Write_1P(0x59,0x67);
Generic_Short_Write_1P(0x5a,0x89);
Generic_Short_Write_1P(0x5b,0xAB);
Generic_Short_Write_1P(0x5c,0xCD);
Generic_Short_Write_1P(0x5d,0xEF);

//GIP_3

Generic_Short_Write_1P(0x5e,0x11);

Generic_Short_Write_1P(0x5f,0x0c);
Generic_Short_Write_1P(0x60,0x0d);
Generic_Short_Write_1P(0x61,0x0e);
Generic_Short_Write_1P(0x62,0x0f);
Generic_Short_Write_1P(0x63,0x06);
Generic_Short_Write_1P(0x64,0x07);
Generic_Short_Write_1P(0x65,0x02);
Generic_Short_Write_1P(0x66,0x02);
Generic_Short_Write_1P(0x67,0x02);
Generic_Short_Write_1P(0x68,0x02);
Generic_Short_Write_1P(0x69,0x02);
Generic_Short_Write_1P(0x6a,0x02);
Generic_Short_Write_1P(0x6b,0x02);
Generic_Short_Write_1P(0x6c,0x02);
Generic_Short_Write_1P(0x6d,0x02);
Generic_Short_Write_1P(0x6e,0x02);
Generic_Short_Write_1P(0x6f,0x02);
Generic_Short_Write_1P(0x70,0x02);
Generic_Short_Write_1P(0x71,0x02);
Generic_Short_Write_1P(0x72,0x02);
Generic_Short_Write_1P(0x73,0x01);
Generic_Short_Write_1P(0x74,0x00);

Generic_Short_Write_1P(0x75,0x0c);
Generic_Short_Write_1P(0x76,0x0d);
Generic_Short_Write_1P(0x77,0x0e);
Generic_Short_Write_1P(0x78,0x0f);
Generic_Short_Write_1P(0x79,0x06);
Generic_Short_Write_1P(0x7a,0x07);
Generic_Short_Write_1P(0x7b,0x02);
Generic_Short_Write_1P(0x7c,0x02);
Generic_Short_Write_1P(0x7d,0x02);
Generic_Short_Write_1P(0x7e,0x02);
Generic_Short_Write_1P(0x7f,0x02);
Generic_Short_Write_1P(0x80,0x02);
Generic_Short_Write_1P(0x81,0x02);
Generic_Short_Write_1P(0x82,0x02);
Generic_Short_Write_1P(0x83,0x02);
Generic_Short_Write_1P(0x84,0x02);
Generic_Short_Write_1P(0x85,0x02);
Generic_Short_Write_1P(0x86,0x02);
Generic_Short_Write_1P(0x87,0x02);
Generic_Short_Write_1P(0x88,0x02);
Generic_Short_Write_1P(0x89,0x01);
Generic_Short_Write_1P(0x8A,0x00);

Generic_Long_Write_3P(0xFF,0x98,0x81,0x04);
Generic_Short_Write_1P(0x68,0xDB);
Generic_Short_Write_1P(0x6D,0x08);
Generic_Short_Write_1P(0x70,0x00);
Generic_Short_Write_1P(0x71,0x00);
Generic_Short_Write_1P(0x66,0x1E);
Generic_Short_Write_1P(0x3A,0x24);
Generic_Short_Write_1P(0x82,0x0A);
Generic_Short_Write_1P(0x84,0x0A);
Generic_Short_Write_1P(0x85,0x16);
Generic_Short_Write_1P(0x32,0xAC);
Generic_Short_Write_1P(0x8C,0x80);
Generic_Short_Write_1P(0x3C,0xF5);
Generic_Short_Write_1P(0xB5,0x02);
Generic_Short_Write_1P(0x31,0x25);
Generic_Short_Write_1P(0x88,0x33);

```
Generic_Long_Write_3P(0xFF,0x98,0x81,0x01);  
Generic_Short_Write_1P(0x22,0x0A);  
Generic_Short_Write_1P(0x31,0x00);  
Generic_Short_Write_1P(0x50,0x5C);  
Generic_Short_Write_1P(0x51,0x5C);  
Generic_Short_Write_1P(0x53,0x5C);  
Generic_Short_Write_1P(0x55,0x61);  
Generic_Short_Write_1P(0x60,0x2B);  
Generic_Short_Write_1P(0x61,0x00);  
Generic_Short_Write_1P(0x62,0x19);  
Generic_Short_Write_1P(0x63,0x00);
```

//Pos Register

```
Generic_Short_Write_1P(0xA0,0x00);  
Generic_Short_Write_1P(0xA1,0x10);  
Generic_Short_Write_1P(0xA2,0x1C);  
Generic_Short_Write_1P(0xA3,0x12);  
Generic_Short_Write_1P(0xA4,0x15);  
Generic_Short_Write_1P(0xA5,0x27);  
Generic_Short_Write_1P(0xA6,0x1D);  
Generic_Short_Write_1P(0xA7,0x1E);  
Generic_Short_Write_1P(0xA8,0x86);  
Generic_Short_Write_1P(0xA9,0x1E);  
Generic_Short_Write_1P(0xAA,0x2B);  
Generic_Short_Write_1P(0xAB,0x79);  
Generic_Short_Write_1P(0xAC,0x1C);  
Generic_Short_Write_1P(0xAD,0x1B);  
Generic_Short_Write_1P(0xAE,0x50);  
Generic_Short_Write_1P(0xAF,0x25);  
Generic_Short_Write_1P(0xB0,0x2A);  
Generic_Short_Write_1P(0xB1,0x4B);  
Generic_Short_Write_1P(0xB2,0x56);  
Generic_Short_Write_1P(0xB3,0x1C);
```

//Neg Register

```
Generic_Short_Write_1P(0xC0,0x00);  
Generic_Short_Write_1P(0xC1,0x10);  
Generic_Short_Write_1P(0xC2,0x1C);  
Generic_Short_Write_1P(0xC3,0x12);
```



```
Generic_Short_Write_1P(0xC4,0x15);
Generic_Short_Write_1P(0xC5,0x27);
Generic_Short_Write_1P(0xC6,0x1D);
Generic_Short_Write_1P(0xC7,0x1E);
Generic_Short_Write_1P(0xC8,0x86);
Generic_Short_Write_1P(0xC9,0x1E);
Generic_Short_Write_1P(0xCA,0x2B);
Generic_Short_Write_1P(0xCB,0x79);
Generic_Short_Write_1P(0xCC,0x1C);
Generic_Short_Write_1P(0xCD,0x1B);
Generic_Short_Write_1P(0xCE,0x50);
Generic_Short_Write_1P(0xCF,0x25);
Generic_Short_Write_1P(0xD0,0x2A);
Generic_Short_Write_1P(0xD1,0x4B);
Generic_Short_Write_1P(0xD2,0x56);
Generic_Short_Write_1P(0xD3,0x1C);
```

```
Generic_Long_Write_3P(0xFF,0x98,0x81,0x00);
Generic_Short_Write_1P(0x35,00);
Generic_Short_Write_1P(0x11,00);
Delays(120);
Generic_Short_Write_1P(0x29,00);
Delays(120);
}
```

```
Void Panel_SleepIn_Mode (void)
```

```
{
LCD_ILI9881D_CMD(0xFF); //Page0
LCD_ILI9881D_INDEX(0x98);
LCD_ILI9881D_INDEX(0x81);
LCD_ILI9881D_INDEX(0x00);
```

```
LCD_ILI9881D_CMD(0x28); //Display Off
Delays(50);
LCD_ILI9881D_CMD(0x10); //Sleep-In
Delays(120);}
```

```
Void Panel_SleepOut_Mode (void)
{
LCD_ILI9881D_CMD(0xFF); //Page0
LCD_ILI9881D_INDEX(0x98);
LCD_ILI9881D_INDEX(0x81);
LCD_ILI9881D_INDEX(0x00);

LCD_ILI9881D_CMD(0x11); //Sleep-Out
Delayms(120);
LCD_ILI9881D_CMD(0x29); //Display On
Delayms(50);
}
```

--END--